

FIGURE 1

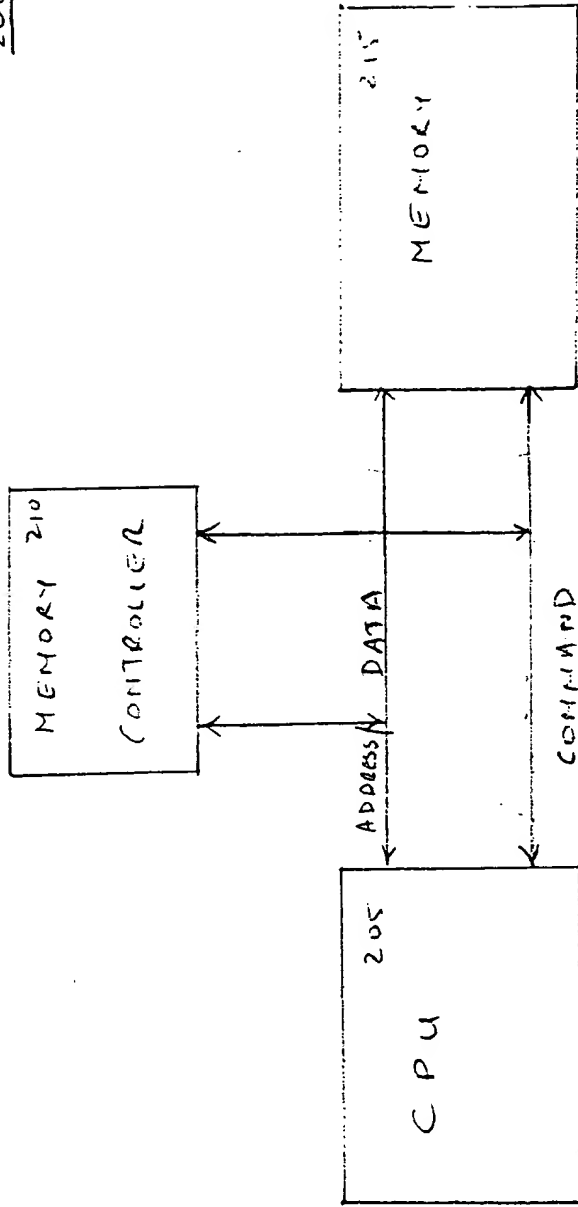


FIGURE 2

CHIP SELECT, BANK SELECT, SDRAM ADDR

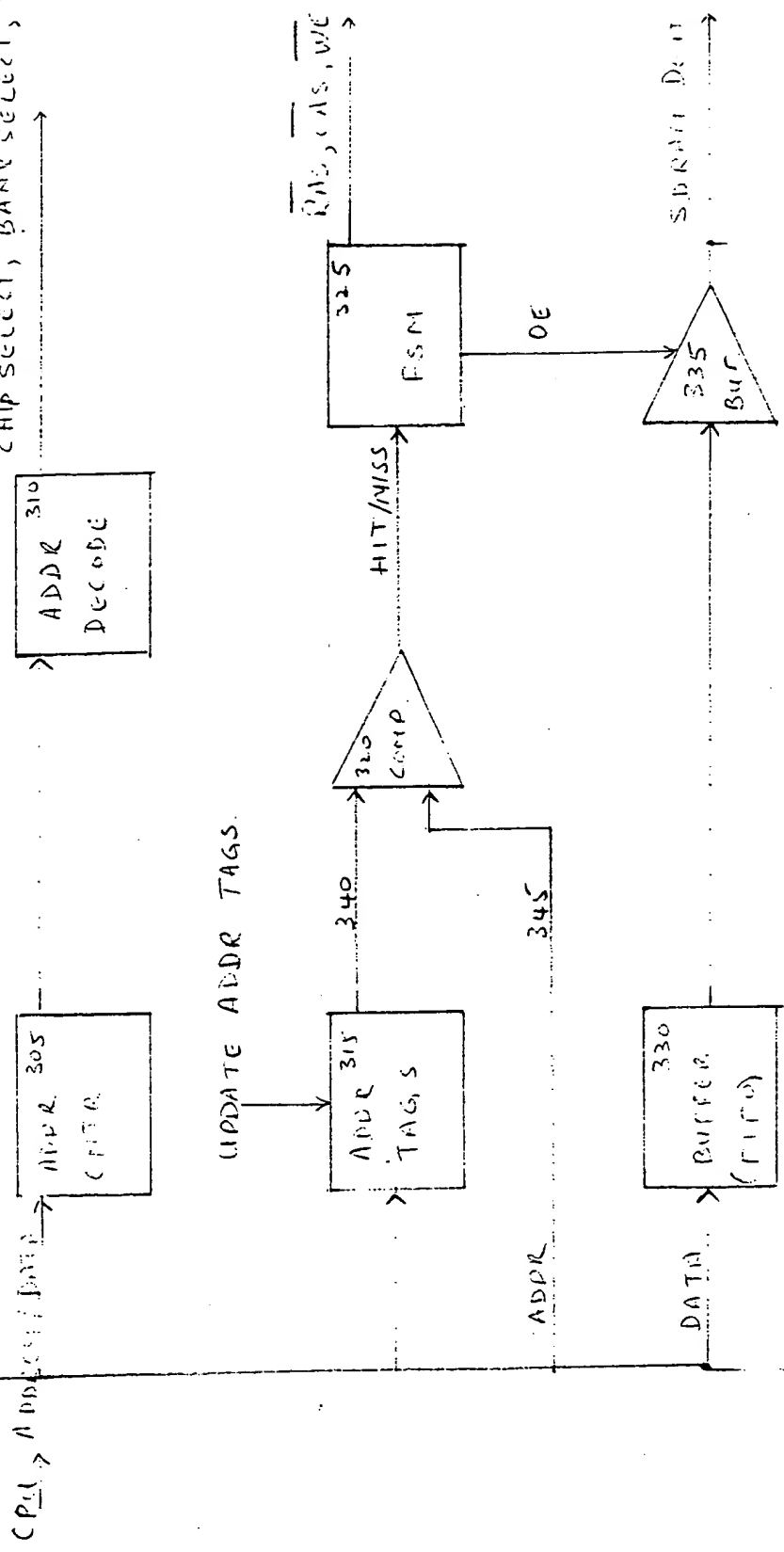


FIGURE 3

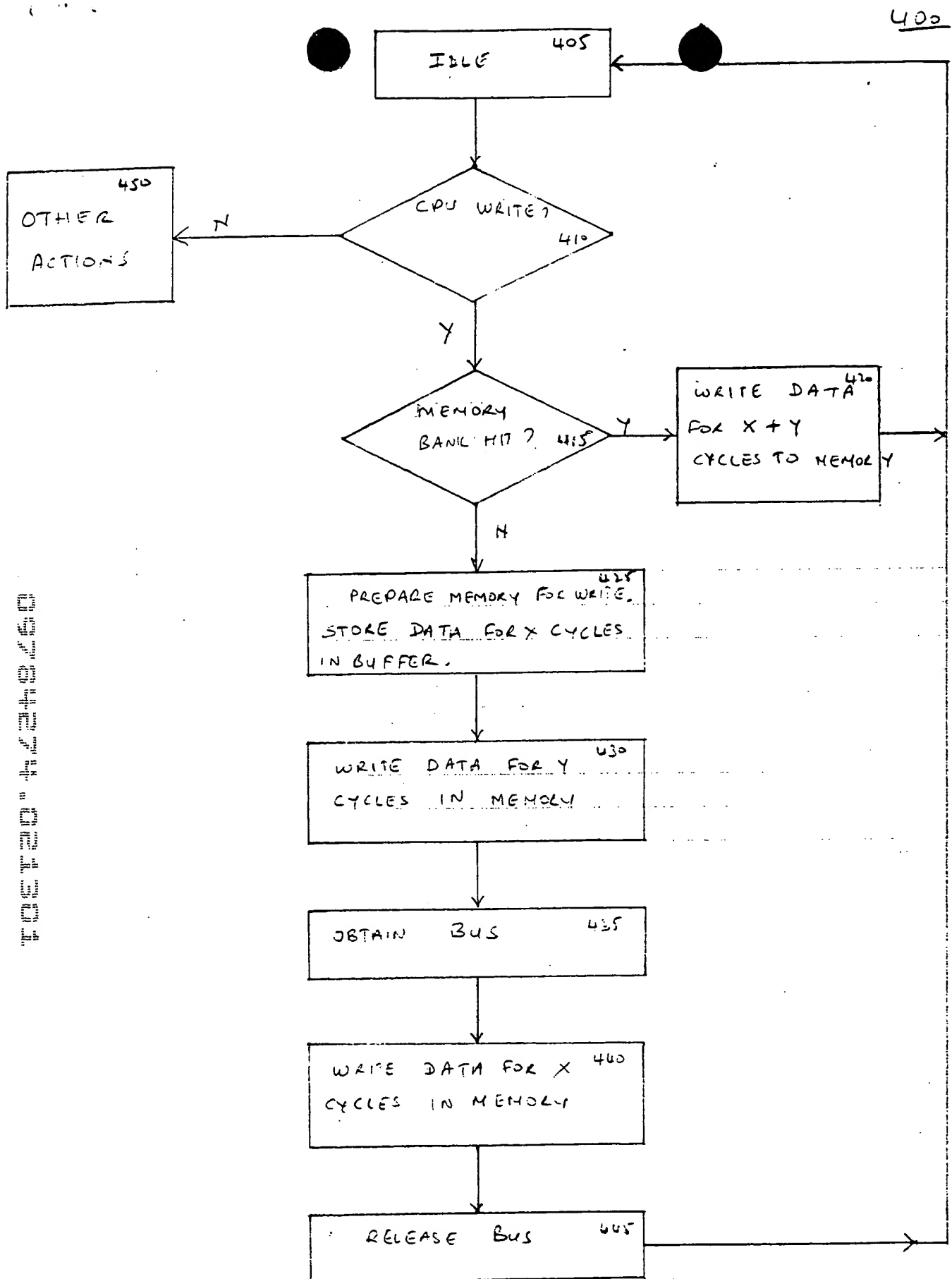


FIGURE 4

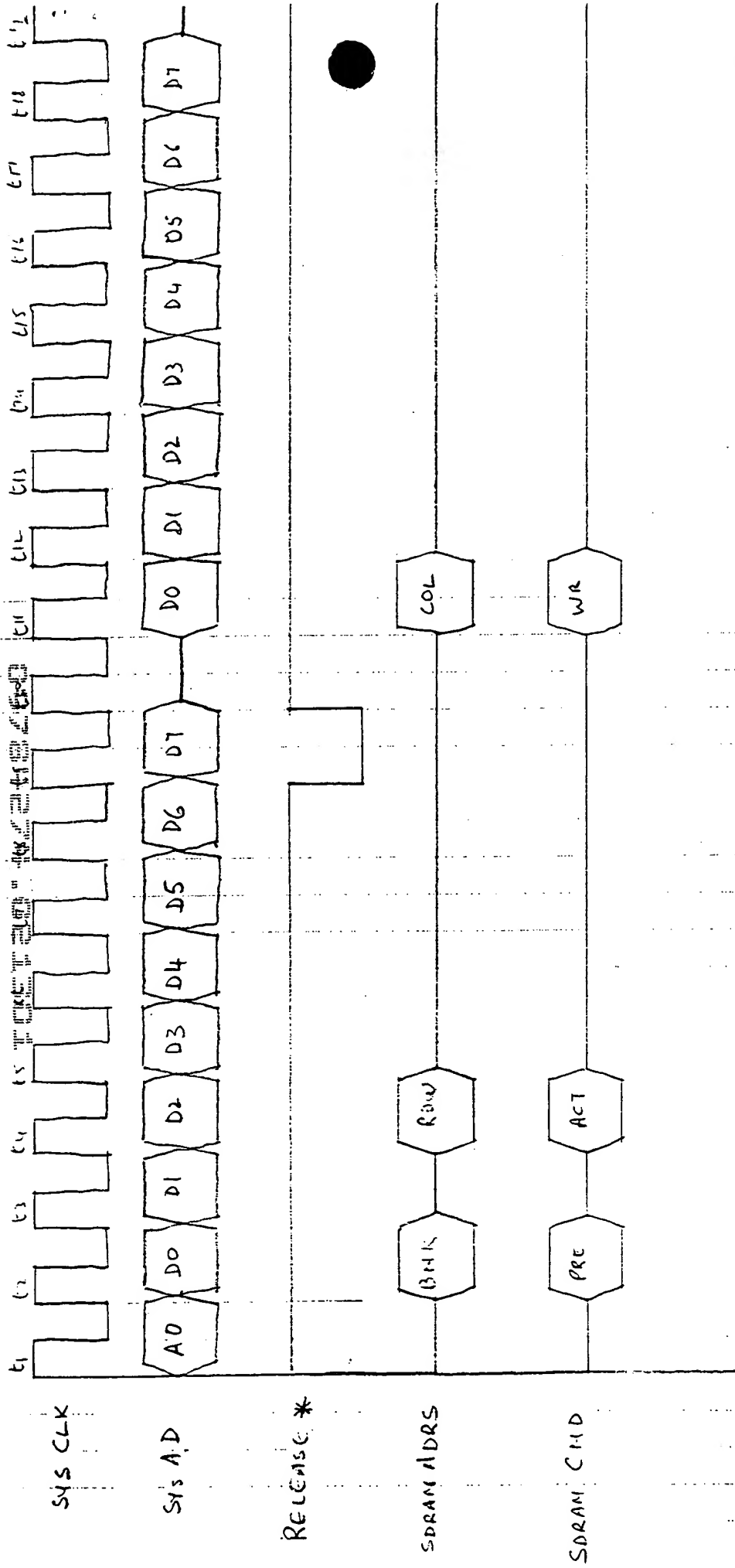


FIGURE 5
(PRIOR ART)

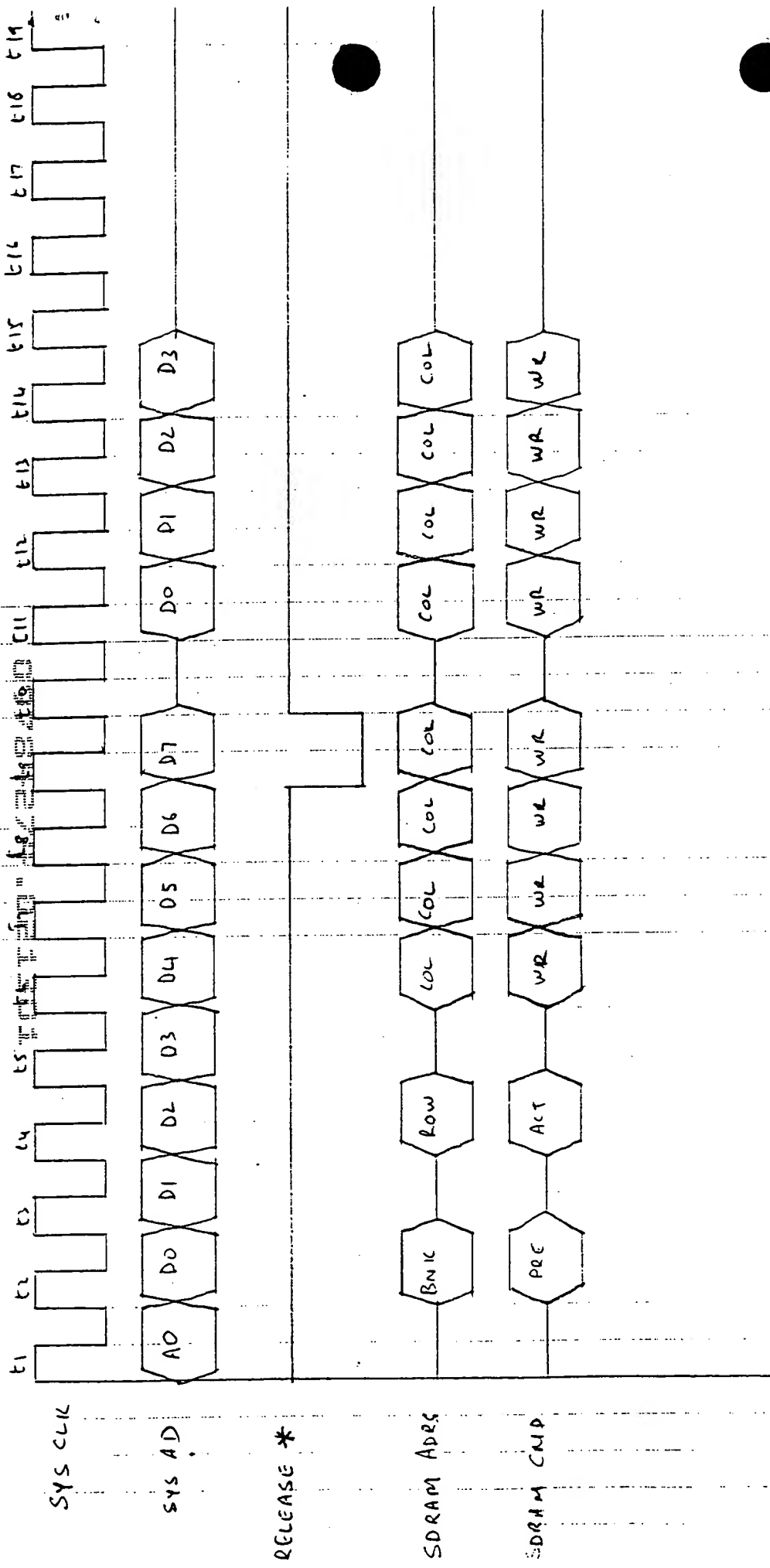


FIGURE 6

700

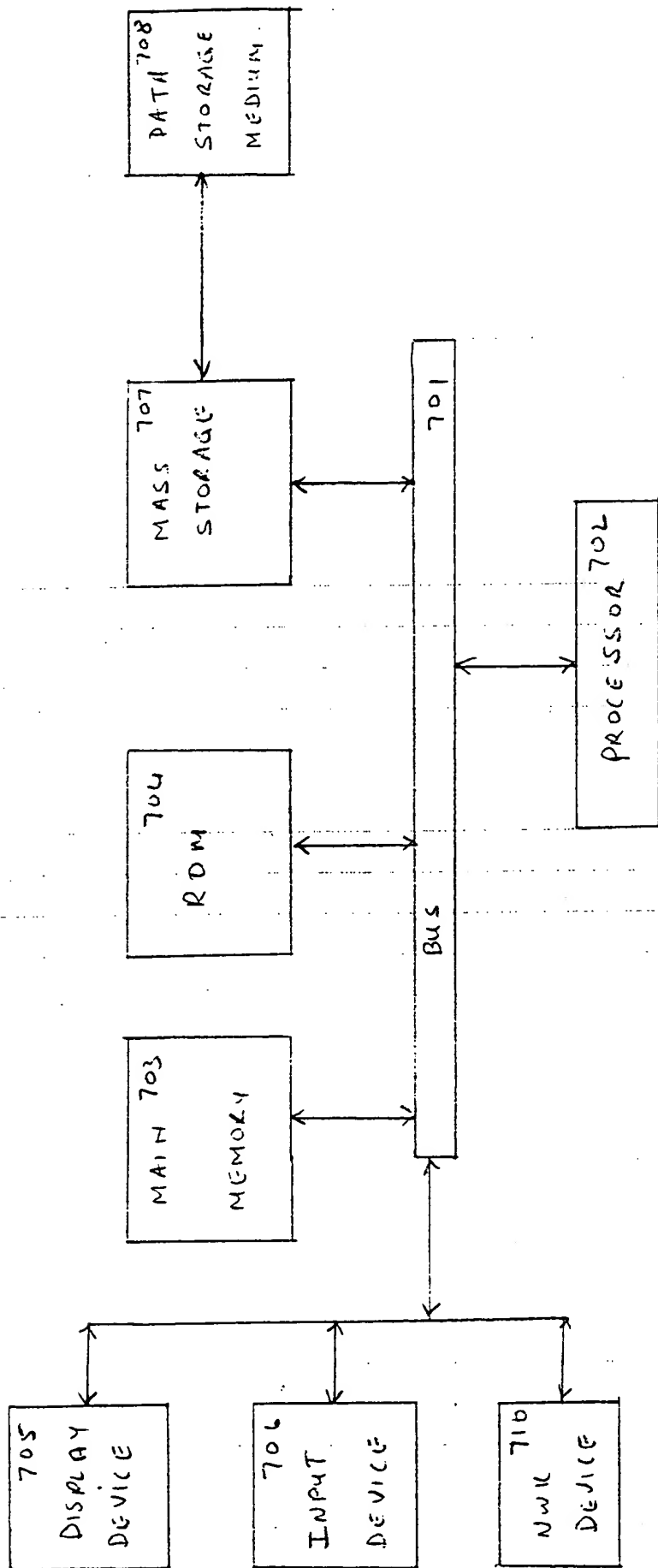


FIGURE 7